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VLSI, digital logic and computer architecture. Between 1999 and 2003, he served as an associate director of the Laboratory for Computer Science. He holds a Ph.D. and an M.S. in Electrical Engineering from Stanford University, and a bachelor's degree in Electrical Engineering from IIT Madras.

LECTURE NOTES ON VLSI DESIGN B.Tech VII semester (R16)

for nearly all digital logic applications. In 1965, Gordon Moore observed that plotting the number of transistors that can be most economically manufactured on a chip gives a straight line on a semilogarithmic scale . At the time, he found transistor count doubling every 18 months. This observation has been called Moore's Law and

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We first consider the discrete case. This case has applications not only in communication theory, but also in the theory of computing machines, the design of telephone exchanges and other fields. In addition the discrete case forms a foundation for the continuous and mixed cases which will be treated in the second half of the paper.

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